

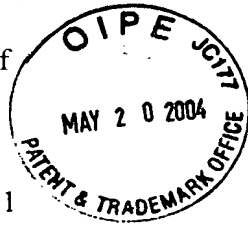
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of

Ritsuko Iwasaki



Serial No.: 09/496,421

Group Art Unit: 2815

Filing Date: February 2, 2000

Examiner: Lee, Eugene

For: SEMICONDUCTOR DEVICE HAVING AN IMPROVED LAYOUT
PATTERN OF PAIR TRANSISTORSHonorable Commissioner of Patents
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

Under the provisions of 37 CFR §1.97 through §1.99 and pursuant to applicant's duty of disclosure under 37 CFR §1.56, applicant respectfully brings the following documents cited in the Japanese Office Action and listed on the attached form PTO-1449, to the attention of the Examiner in charge of the above-identified application. Copies of the listed documents are provided herewith for the convenience of the Examiner.

In compliance with the requirements of 37 CFR §1.98(a)(3), as a concise statement of relevance, as it is presently understood by the individual designated in 35 U.S.C. §1.56(c) most knowledgeable about the content of the information, the undersigned attorney of record submits a translation of portions of an official action by a foreign examiner in which the reference was cited. The relevance to the pending U.S. patent application is that the reference was cited in a foreign patent application on the same subject matter. However, no independent analysis of the reference, the accuracy of the statement of the foreign examiner or the claims of the foreign application under the laws of that country or the United States relative to the subject matter claimed in the present application has been made, the present understanding of the contents thereof by the undersigned being based on the translation of the foreign examiner's comments submitted herewith.

This citation does not constitute an admission that the references are relevant or material to the claims. They are only cited as constituting related art of which the applicant is aware.

09/496,421

Docket No.: 24705/99

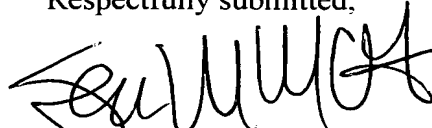
I hereby certify that each item of information contained in this Information Disclosure Statement was the first citation of that item by a foreign patent office in a counterpart foreign application, which occurred not more than three months prior to the filing of this statement.

Submitted herewith is the IDS fee of \$180.00.

It is respectfully requested that the listed references be considered by the Examiner and formally made of record in this application.

Please charge any deficiencies in fees and credit any overpayment of fees to Attorney's Deposit Account No. 50-0481.

Respectfully submitted,



Sean M. McGinn

Registration No. 34,386

Date: 5/20/04

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INFORMATION DISCLOSURE CITATION <small>*(Use several sheets if necessary)*</small>				Docket Number (Optional) 24705/99		Application Number 09/496,421	
				Applicant(s) Ritsuko Iwasaki			
				Filing Date February 2, 2000		Group Art Unit 2815	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS								
	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		2-121366	05/09/1990	Japan			ABS	
		2-246373	10/02/1990	Japan			ABS	
		10-200109	07/31/1998	Japan			ABS	

OTHER DOCUMENTS <small>(Including Author, Title, Date, Pertinent Pages, Etc.)</small>		
		Japanese Office Action dated March 9, 2004 with Partial English Translation.

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.